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Alexandria, VA 22313-1450

on <u>5/14/03</u>

TOWNSEND and TOWNSEND and CREW LLP

By: Lisa Larger

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Examiner:

Lourdes C. Cruz

Dennis Lang

Art Unit:

2827

Application No.: 10/052,945

AMENDMENT

Filed: November 2, 2001

For: SEMICONDUCTOR PACKAGES FOR SEMICONDUCTOR DEVICES

Mail Stop: Non-Fee Amendment

Commissioner For Patents

P.O. Box 1450

Alexandria, CA 22313-1450

Sir:

In response to the Office Action mailed on March 19, 2003, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Please replace the paragraph at page 1, line 10 with the following paragraph.

--[03] A simplified illustration of a portion of an LDMOS RF power transistor package is shown in FIG. 1. FIG. 1 shows a semiconductor die 11 with an N+